

Please cancel Claim 20.

REMARKS

Claims 8, 12 and 20 have been cancelled. Thus, Claims 1-7, 9-11 and 13-19 are currently pending in the present application, of which Claims 1-7, 9-11, 13 and 17-19 have been amended.

Formal drawings will be submitted upon allowance of the application.

Rejection under 35 U.S.C. § 102

Claims 1, 3-6, 9-10, 14, 17 and 19 were rejected under 35 U.S.C. § 102(e) as being anticipated by *Nelson et al.* (US 6,018,232). Applicants respectfully traverse such rejection insofar as it might be applied to the claims as amended herein.

On page 8 of the Office Action, the Examiner has indicated that Claims 7, 8, 12, 13, 18 and 20 would be allowable if rewritten in independent form including all of the limitations of the based and any intervening claims. Because the contents of Claims 8, 12, and 20 have been incorporated within Claims 1, 10, and 17, respectively; thus, the § 102 rejection is believed to be overcome.

CONCLUSION

Claims 1-7, 9-11 and 13-19 are currently pending in the present application.

For the reasons stated above, Applicants believe that independent Claims 1, 10 and 17 along with their respective dependent claims are in condition for allowance. The remaining prior art cited by the Examiner but not relied upon has been reviewed and is not believed to show or suggest the claimed invention.

No fee or extension of time is believed to be necessary; however, in the event that any fee or extension of time is required for the prosecution of this application, please charge it against Deposit Account No. **50-0563**.

Respectfully submitted,



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IN THE CLAIMS

1. (Amended) A method for receiving a wireless signal[, in] by a computer adapted to operate in a power-saving mode, [while the computer is in the power-saving mode,] said method comprising the steps of:

detecting within a computer a wireless signal representing a bit sequence when said computer is operating in a power-saving mode, wherein said wireless signal is targeted for said computer; [and]

exiting said power-saving mode automatically in response to said [detection of said] wireless signal;

regenerating some or all of said bit sequence of said wireless signal; and

storing said some or all of said bit sequence of said wireless signal in [said] a memory after exiting said power-saving mode [is exited].

2. (Amended) The method of claim 1 [for receiving a wireless signal, wherein said computer includes an wireless signal receiver device, and said computer includes one or more status signals indicating whether said wireless signal receiver device is installed and enabled, wherein the step of exiting said power savings mode automatically in response to said detection of said wireless signal], further includes the steps of:

determining whether [said] a wireless signal receiver device is installed and enabled by reading [said] a plurality of status signals; and

exiting said power-saving mode[,], only if said wireless signal receiver device is installed and enabled.

3. (Amended) The method of claim 1 [for receiving a wireless signal], wherein [the step of] said detecting [a wireless signal targeted for said computer,] further includes [the steps of:] detecting a particular identification tag embedded in said bit sequence.

4. (Amended) The method of claim 1 [for receiving a wireless signal], wherein wireless signal is transmitted through a radio frequency channel.

5. (Amended) The method of claim 1 [for receiving a wireless signal], wherein said bit sequence includes a request for said computer to exit said power-saving mode.

6. (Amended) The method of claim 1 [for receiving a wireless signal], wherein said bit sequence includes a request to continue execution of a program that is suspended while said computer is in said power-saving mode.

7. (Amended) The method of claim 1 [for receiving a wireless signal], wherein said computer comprises a receiving means for detecting said wireless signal, and said computer further comprises a switch for maintaining power to said receiving means while operating in power-saving mode, and further comprising the step of:

setting said switch to maintain power to said receiving means prior to entering said power-saving mode.

8. cancelled

9. (Amended) The method of claim 1 [for receiving a wireless signal], further includes [comprising] the steps of:

processing information conveyed by said bit sequence; and

returning to said power-saving mode.

10. (Amended) A computer for receiving a wireless signal while in a power-saving[s] mode, said computer comprising:

a receiving means adapted to detect a wireless signal representing a sequence of bits, wherein said wireless signal is targeted for said computer; [and]

a power-saving mode control means adapted to exit said power-saving mode[, when] in response to a detection of said wireless signal [is detected while] when said computer is in [a] said power-saving mode; and

a switch for enabling power to said receiving means [while] when said computer is in said power-saving mode.

11. (Amended) The computer of claim 10, further [comprising] includes:

one or more status [signals] indicators for indicating whether said receiving means is installed and enabled; and

wherein said power-saving mode control is adapted to exit said power-saving mode, only if said one or more status indicators show that said receiving means is installed and enabled.

12. cancelled

13. (Amended) The computer of claim 10, further [comprised of] includes:

a memory for storing bits;

wherein said receiving means is adapted to regenerate some or all of said bit sequence; and

wherein said computer is adapted to store said regenerated some or all of said bit sequence in said memory when said computer has exited said power-saving mode.

14. (unchanged) The computer of claim 10, wherein said receiving means is an optional attachment to said computer.

15. (unchanged) The computer of claim 10, wherein said receiving means is formed in a device bay cover.

16. (unchanged) The computer of claim 15, wherein said device bay cover is an optional attachment to said computer.

17. (Amended) An computer, comprising:

a docking station [receiving means within said computer,] for receiving a signal representing a bit sequence;

a power saving mode selection means [within said computer,] for selectively entering and exiting a power-saving mode; and

a detection means within said docking station [receiving means,] for detecting a wireless signal targeted for said computer while said computer is in a power-saving mode; and

a control means within said power saving mode selection means for exiting said power-saving mode in response to said detected wireless signal.

18. (Amended) The computer of claim 17, further includes

[wherein] means for disabling at least one power source [is disabled while] when said computer is in said power-saving mode; wherein said receiving means asserts a wake up signal to said [power-saving mode] control means to indicate said detected wireless signal [is received which] is targeted for said computer; and

[wherein said] a power management circuit to enable at least one power source, in response to said asserted wake up signal [enables said least one power source].

19. (Amended) The computer of claim 17, wherein said receiving means is an option card [communicatively] coupled to said computer through an option card bus slot.

20. cancelled